

## HOW TO APPLY

- A duly filled in application form in the prescribed form signed by the Head of the Institute to which the candidate belongs (along with demand draft/CBS-Cheque) should reach by post to the local coordinator of the participating academy.
- Government of India norms will be followed for SC/ST category participants

### Address for Correspondence:

Dr. Usha Mehta/Dr. Akash Mecwan  
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## NIRMA UNIVERSITY

Nirma University was established in the year 2003 as a statutory university under Gujarat State Act at the initiative of Nirma Education and Research Foundation (NERF). In 1994, founder of Nirma Industries and an internationally renowned entrepreneur Dr Karsanbhai K Patel crystallized his long cherished dream of providing world class facilities for professional education in Gujarat. He established NERF which in turn established, a 125 acre campus, six institutions within a span of 15 years. Functioning under the aegis of NERF, the university presently includes of a top grade – Institute of Technology (1995), a leading business school – Institute of Management (1996), an innovative Institute of Diploma Studies (1997), an upcoming Institute of Pharmacy (2003), Institute of Science (2004) and emerging Institute of Law (2007). All these institutions have emerged as centers of excellence offering various programmes ranging from undergraduate to doctoral levels in technology, engineering, business, pharmacy and sciences.

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION

The Electronics and Communication Engineering Department was started in 1995. The Department offers Under Graduate (UG), Post Graduate (PG) programs & Ph. D Programme. The department has active research interest in various domains of electronics and communication engineering. The Department is firmly committed to excellence in teaching and learning. The Department is equipped with state-of-art laboratories and it provides excellent facilities to carry out research work and consultancy projects.

The Department has large pool of experience faculties in academics and research. Most of the faculties are Ph.D. holders and remaining are pursuing Ph. D. The research interests of the faculty members encompass a wide gamut of sub-disciplines of EC Engineering. Collaboration with faculty members from other disciplines, both within and outside the institute, is encouraged. The faculty members of EC bagged plenty of awards towards Academic Excellence.

The research activity of the department includes fundamental research, sponsored and consultancy projects, and is carried out with the active participation of students, faculty members, and R & D organizations. Over the years, the department has acquired expertise in different areas of EC.

## APPLICATION FORM

The application form for the Faculty Development Programme is available on the website of the Ministry of Electronics and Information Technology (MeitY). The same can be downloaded from:

[http://mnit.ac.in/cms/uploads/2019/11/Global\\_NKN\\_FDPs\\_Winter\\_2019\\_-\\_20.pdf](http://mnit.ac.in/cms/uploads/2019/11/Global_NKN_FDPs_Winter_2019_-_20.pdf)



## Faculty Development Program On VLSI Chip Design Hands on using Open Source EDA

**December 16 – 20, 2019**

Coordinators  
Dr. Usha Mehta  
Dr. Akash Mecwan

### Sponsored By:



Ministry of Electronics and Information  
Technology, Government of India  
Initiative for Employability  
Enhancement

### Organized by

**Department of Electronics and  
Communication Engineering  
Institute of Technology,  
Nirma University**

Website:

[http://mnit.ac.in/eict/acad\\_training\\_prg.php](http://mnit.ac.in/eict/acad_training_prg.php)

## **ABOUT THE PROGRAM**

Faculty Development Programmes in core areas of Electronics and Information & Communication Technology (ICT) streams have been planned by academies for delivery during winters (i.e., Dec'19 – Jan'20). All these winter courses will be offered through National Knowledge Network (NKN) based Video Conferencing, with lectures delivered by invited experts from IITs, NITs, IIITs and other premier institutes / industries. In addition, local course coordinators at respective academies / identified remote centers will take care of sessions on design orientation / activity linked problems / assignments / case studies and quiz test(s). All seven EICT Academies will host the participants simultaneously along with some select remote centres all over our country, through NKN-VC infrastructure. Candidates could attend the training programme at Academy locations or at identified remote centers as per the convenience. For registration participants need to apply to one of the Academies, however, they can attend the training programme at that Academy or any remote centre attached to that respective Academy, please refer to respective academy websites.

## **OUTLINE OF CONTENT**

- ✓ Basics of RISC – V Processor based SoC design using MAGIC Tool.
- ✓ Standard cell library design
- ✓ Pre-layout timing analysis, chip planning and block level routing using open source EDA
- ✓ Hierarchical placement/routing and LVS/DRC using MAGIC
- ✓ Post-layout timing analysis

## **RESOURCE PERSONS**

The workshop will be conducted by experts from IITs and NIT and experts from SCL Chandigarh.

## **IMPORTANT DATES**

Receipt of Application : 09/12/2019  
Confirmation of Selection : 11/12/2019  
By Email  
Commencement of Course : 16/12/2019

## **FOR WHOM**

- ✓ Faculty Members of Engineering Colleges
- ✓ Faculty Members of Polytechnics
- ✓ Research Scholars of Technical Institutes

## **COURSE REGISTRATION FEES**

There are no registration fees for the course. The course is fully sponsored by Government of India. However, a Demand Draft (DD) or CBS-Cheque of Rs. 1000/-, payable to "Electronics & ICT Academy, MNIT Jaipur" is required to register in the course. The same will be returned to the participants on the last day of the training. Certification of participation as well as for satisfactory performance will be given to the participants subject to fulfillment of attending all sessions, submission of assignments and clearing of tests.

Tea, lunch and course materials will be provided to all the participants. Participants will be provided with chargeable boarding facilities on request on first come first serve basis. Participants will have to make their own arrangements for travelling.

**For further details, contact:**  
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## **MeitY OBJECTIVES**

India is fast emerging as a world power in Information, Communications Technology and Electronics Sectors (ICTE). To complement its growth and further development, there is an ever increasing need for trained professionals with specialization in this space. This includes training of professionals not only in existing and changing technologies but also in the fields of R & D and electronics manufacturing. This will specifically be aimed at the ICTE sector to create a substantial resource pool of talent and generate ample opportunities for entrepreneurs.

Ministry of Electronics and Information Technology has approved a scheme and setup Electronics and ICT Academies at seven institutes viz. IIT Guwahati, IIT Kanpur, NIT Warangal, NIT Patna and IITDM Jabalpur and IIT Roorkee, MNIT Jaipur. The ministry had earlier setup two ICT academies at Tamilnadu and Kerala respectively.

These academies are aimed at faculty /mentor development and up gradation to improve the employability of the graduates, diploma holders in various streams, through collaboration of States/union territories. Each Academy is being provided funding support for four years and is expected to generate revenue by charging fee and taking up other activities to meet the recurring cost in gradual manner and become self-sustainable by the end of fourth year onwards. All these academies will cater to the requirements of identified neighboring state and UTs also.