

NIRMA UNIVERSITY
SCHOOL OF TECHNOLOGY, INSTITUTE OF TECHNOLOGY
Teaching & Examination Scheme
M.Tech. in Electronics & Communication Engineering (VLSI Design)

Semester-III

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3EC1307 Major Project: Part – I

Course Outcomes (COs):

At the end of the course, the students will be able to

1. Understand the issues related to the recent trends in the field of engineering and its applications
2. Formulate the problem definition, analyze and do a functional simulation of the same
3. Design, Implement, test and verify the engineering solution related to problem definition
4. Compile, Comprehend and Present the work carried out
5. Manage Project

A student is required to carry out elaborated project work. The project may be either design and fabrication work or simulation and synthesis of a problem/system, develop algorithms, and verify the feasibility on a computer. At the end of the semester, the student will be required to submit a detailed report of a literature survey, design problem formulation, analysis, functional simulation and synthesis, work plan, and work done and will defend his/her work carried out before examiners.