

MTECH ADMISSIONS 2024



VLSI Design | Embedded Systems | Semiconductor Technology

INSTITUTE OF TECHNOLOGY, NIRMA UNIVERSITY

The Institute of Technology, established in 1995, offers multidisciplinary undergraduate, postgraduate and doctoral programmes in the discipline of Engineering and Technology. It is NAAC 'A+' grade accredited, ranked 125th among 4000 engineering colleges by NIRF, and in the top 15 in the country amongst self-financed colleges. All BTech Programmes are accredited by the National Board of Accreditation (NBA) under Tier-I category.

ELECTRONICS & COMMUNICATION DEPARTMENT

The Electronics and Communication Engineering Department established in 1995 is known for its research in VLSI Design and Embedded Systems. The Department focuses equally on academics and research, thereby enabling the students to build a strong profile. Through blended learning techniques, students are exposed to theory supported by hands-on training in the laboratories. The Department offers undergraduate, postgraduate and doctoral programmes with specialisation in VLSI Design, Semiconductor Technology and Embedded Systems. It has a pool of well-qualified and experienced faculty, well-equipped laboratories, meritorious students and industrial connection. An opportunity to work with Professors on live research projects, a challenging peer group, placements in top-notch companies and a disciplined teaching-learning environment are the highlights of the department.

MTECH PROGRAMS OFFERED BY EC DEPARTMENT:

The Department offers MTech in three specialisations:

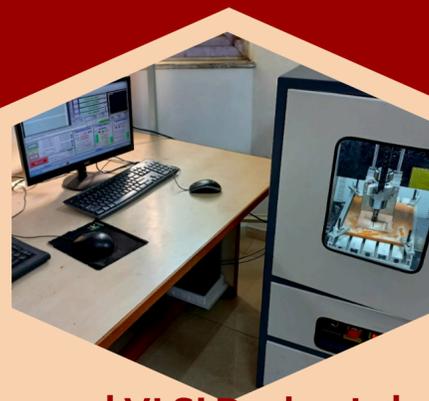
- MTech in VLSI Design (24 Intake)
- MTech in Semiconductor Technology (18 Intake)

MTech in Embedded Systems (18 Intake)

MTech is a two-year programme with core and advanced level courses and laboratory work followed by a one-year project with a strong research component. The project work is carried out in industries, R&D Organisations or in-house. The qualifying exam is Degree in EC/IC/CE/IT/EE or equivalent.

ELIGIBILITY:

Admission to MTech programs are based on merit of GATE(EC) score, in case of remaining seats based on merit of admission test (equivalent to GATE) conducted by the department.



Department has MeITY, Govt. of India, Sponsored Advanced VLSI Design Laboratory

SALIENT FEATURES OF THE MTECH PROGRAM AT EC DEPARTMENT

MOU WITH CSIR-CEERI(PILANI), SEMICONDUCTOR COMPLEX LIMITED (CHANDIGARH), SAC-ISRO

State of the art laboratories- VLSI Design, Embedded systems, FPGA-based design, etc.

Advanced industry based EDA Tools such as Cadence, Siemens Mentor Graphics, Xilinx, Proteus, MATLAB, etc.

Advanced FPGA Boards, embedded systems, edge computing, Machine Learning, Cyber Physical Systems, GPGPU Systems

Consistent placement track record in reputed MNCs in the domains of VLSI & Embedded systems

Strong collaboration and MoUs with industry and research institutions

Funded research projects from DRDO, ISRO, DST, GUJCOST, Nirma University

Laboratory and Industry visit to SCL, CEERI, SAC-ISRO, IIT Mumbai, Adani Solar, eInfochips, etc.

PLACEMENT PARTNERS:



Contact us:

hod_ec.it@nirmauni.ac.in

<https://ec.nirmauni.ac.in/>

<https://www.linkedin.com/company/ec-it-nu/>