



NU/AC/19(3)/Insti/MT-ST/24- 52
Date: 22.05.2024

NOTIFICATION

Read: 1. Notification No. NU-856 dated 04.03.2005 - Institution of New Course Studies (as amended)
2. Notification No. NU-75 dated 23.04.2004 - Regulations for Institutions & Conferment of Degrees/ Diplomas (as amended)
3. Approval of Director General on note dated 20.05.2024

Sub: Institution of a new course of study- M.Tech. in Semiconductor Technology under Faculty of Technology & Engineering along with eligibility criteria and intake

It is hereby notified for information of all concerned that the Director General with the approval of the Chairman, Board of Governors of the University and in exercise of power conferred upon him under Section 19(3) of the Nirma University Act, has approved institution of a new course of study - M.Tech. in **Semiconductor Technology** under Faculty of Technology & Engineering, to be offered from academic year 2024-25 onwards, as under:

I. Institution of New Courses of Study at the University under different Faculties
(published vide notification referred at serial 1 above)

A new course- Master of Technology in **Semiconductor Technology** be added as '(v)' under '1. Faculty of Technology & Engineering, 2) Master of Technology (Regular) in:' (the amendments are shown in **bold** letters):

Sr.	Faculty under which course is instituted	Name of the course	Duration (Years)	Nomenclature of the Degree	Commencement Year
1.	2) Master of Technology in:	(v) Master of Technology in Semiconductor Technology	2	M. Tech in concerned field	2024-25

II. Institution and Conferment of Degrees/Diplomas

(published vide notification referred at serial 2 above)

A new course- Master of Technology in **Semiconductor Technology** be added as sub-clause 'xiii.' under 'R.CON.-1, Degrees, b.(i) @' under 'Faculty of Technology & Engineering' (the amendments are shown in **bold** letters):

R.CON.-1 Degrees

1. Faculty of Technology and Engineering

@ B. Tech and M. Tech shall be awarded with one of the following disciplines:

xiii. Semiconductor Technology

III. Academic Regulations and Eligibility Criteria:

The academic regulations and eligibility criteria applicable to the existing M.Tech. programmes shall be made applicable mutatis-mutandis to M.Tech. in Semiconductor Technology also.

IV. Intake: 18

Executive Registrar

To,

1. Director (SoT)
2. Head, Dept. of CE
3. All Heads of Institute
4. Chief Accounts Officer
5. Dy. Registrar (Exam)

Copy to,

1. Asst. Registrar/OS
2. Statistician
3. Publication Officer/Webmaster
4. Establishment Sec.
5. P.A. to ER

c.f.w.c. to: 1. Vice President; 2. Director General