# MTECH ADMISSIONS 2026

## VLSI Design | Embedded Systems | Semiconductor Technology

NIRMA UNIVERSITY
INSTITUTE OF TECHNOLOGY
NAAC ACCREDITED 'A+' GRADE

## INSTITUTE OF TECHNOLOGY, NIRMA UNIVERSITY

Nirma University, Ahmedabad, was established in the year 2003 as a statutory university under the Gujarat State Act at the initiative of the Nirma Education &amp; Research Foundation. It is recognized by the University Grants Commission (UGC) under section 2(f) of the UGC Act. The university received the Centre of Excellence (CoE) status from the Government of Gujarat in January 2022. The University is also accredited 'A+' grade by the National Assessment and Accreditation Council (NAAC) during its third cycle.

## ELECTRONICS & COMMUNICATION ENGG. DEPARTMENT

The Electronics and Communication Engineering Department established in 1995 is known for its research in VLSI Design and Embedded Systems. The Department focuses equally on academics and research, thereby enabling the students to build a strong profile. Through blended learning techniques, students are exposed to theory supported by hands-on training in the laboratories. The Department offers undergraduate, postgraduate and doctoral programmes with specialisation in VLSI Design, Embedded Systems and Semiconductor Technology. It has a pool of well-qualified and experienced faculty, well-equipped laboratories, meritorious students and industrial connection. An opportunity to work with Professors on live research projects, a challenging peer group, placements in top-notch companies and a disciplined teaching-learning environment are the highlights of the department.

## MTECH PROGRAMS OFFERED BY EC ENGG. DEPARTMENT

### ELIGIBILITY

**VLSI Design** Intake 24

**Embedded Systems** Intake 18

**Semiconductor Technology** Intake 18

The bacholor degree in EC, ICT, EI, EE, CSE with minimum 50% marks.

Admission to MTech programs are based on merit of GATE (EC) score, the remaining seats will be filled based on merit of admission test (equivalent to GATE) conducted by the department. Minimum requirement of GATE Score is applicable as per the category (Open/SC/ST/SEBC/EWS)

**SCHOLARSHIP:** The first 50% GATE candidate of each of three programms will receive scholarship of Rs. 12,400/- per month. Other remaining GATE and Non GATE admitted candidates will recive Rs. 8000/- per month.

**ONLINE Application Process Begins:** 9th March 2026  **ONLINE Application Process Ends:** 15th June 2026, For More Details Click Here



**Department has MeiTY, Govt. of India, Sponsored Advanced VLSI Design Laboratory**

## SALIENT FEATURES OF THE MTECH PROGRAM AT EC DEPARTMENT

- State of the art laboratories- VLSI Design, Embedded systems, FPGA-based design, etc.
- Industry based advanced EDA Tools such as Cadence, Siemens Mentor Graphics, Xilinx, Proteus, MATLAB, etc.
- Hands on training on semiconductor fabrication at CEERI, Pilani.
- Consistent placement track record in reputed MNCs in the domains of VLSI & Embedded systems
- Strong collaboration and MoUs with industry and research institutions
- Funded research projects from MeitY, DRDO, ISRO, DST, GUJCOST, Nirma University
- Industry visit to Semiconductor Laboratory (SCL), Chandigarh, SAC ISRO, eInfochips, etc.

## PLACEMENT PARTNERS

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