

TENTATIVE AREAS FOR PH.D. ADMISSIONS – 2025-26

(Visvesvaraya Ph.D Scheme :(C2S:Chip to Startup Category)), MeiTy, Govt of India

Faculty of Technology and Engineering (Full-Time)

Electronics & Communication Engineering	<ul style="list-style-type: none">• Hardware security, VLSI Design, Test and Verification• RF MEMS Design, Analog VLSI Design, Embedded system• Analog Mixed Signal VLSI design,• Machine Learning and Artificial Intelligence• Memory Design and Testing• In Memory Computing(IMC)• Chip Design for EDGE applications	02
	Total Seats	02