Institute:	Institute of Technology, School of Technology	
Name of Programme:	MTech CSE (Cyber Security)	
Course Code:	6CS261ME25	
Course Title:	Embedded System and Security	
Course Type:	Department Elective-III	
Year of Introduction:	2025-26	

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Credit Scheme									
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At the en 1. exp 2. exp 3. exp the 4. app	Learning Outcomes (CLO): d of the course, students will be able to – blain the core concepts related to embedded syst blain the security and trust aspects of the embed bloit the vulnerabilities related to embedded sy -art tools and technologies bly countermeasures against the introduced attac	tem s ded s ystem	ecuri yster	ty n	-of- ((BL1) (BL2) (BL3) (BL3)			
Unit	Unit Contents		Teaching						
Unit-I Unit-II Unit-III	 Embedded system software, Device drivers and interrupt services, Interprocess communication and synchronization of processes Unit-II Embedded System Security and Trust: Physical attacks, Side- channel analysis, Trusted integrated circuit, Trusted platform module (TPM), Hardware Trojans, Cryptographic hashing, Stack-based attacks against embedded systems (Code injection and return-oriented programming), Physically unclonable functions, Fault injection attacks, Reverse engineering, Supply chain security and trust 						Hours (Total 45) 04 08 08		
Unit-IV	monitoring and decoding, Access via JTAG Embedded Software Security and Exploit embedded software security, Common fi Software vulnerabilities in ARM/MIPS/ vulnerabilities, Assembly code analysis, Exp ARM/MIPS/x86, Defenses against ARM exp for embedded software, Defensive software hardware interfaces	irmw etc., ploita ploits	are Em ition , Sec	vulnerab bedded techniqu curity pr	code code ues on actices		10		

Self-Study:

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The self-study contents will be declared at the commencement of the semester. Around 10% of the questions will be asked from self-study content.

Suggested Readings/ References:

- 1. Tehranipoor, Mohammad; Wang, Cliff, Introduction to Hardware Security and Trust, Springer
- 2. David Kleidermacher and Mike Kleidermacher, Embedded Systems Security: Practical Methods for Safe and Secure Software and Systems Development, Elsevier Science, Newnes Publication
- 3. Louis Goubin and Mitsuru Matsui, Cryptographic Hardware and Embedded Systems, Springer-Verlag Berlin and Heidelberg GmbH & Co.

Suggested List of Experiments:

Sr. No.	Name of Experiments/Exercises	Hours
1	Blinking LEDs Using a Microcontroller	2
2	Reading Sensor Data Using I2C Communication	2
3	Interrupt Handling in Embedded Systems	4
4	Secure Boot Implementation	4
5	Reverse Engineering with JTAG	4
6	Cryptographic Hashing for Data Integrity	4
7	Implementing Stack Canaries for Buffer Overflow Protection	2
8	Firmware Vulnerability Analysis Using Binwalk	4
9	Bus Monitoring and Decoding Using Logic Analyzers	4
10	Simulating a Fault Injection Attack on an Embedded System.	2

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