

**NIRMA UNIVERSITY**  
**SCHOOL OF TECHNOLOGY, INSTITUTE OF TECHNOLOGY**  
**M.Tech. in Electronics & Communication Engineering (VLSI Design)**  
**M.Tech. Semester - II**

L	T	Practical component				C
		LPW	PW	W	S	
-	-	10	-	-	-	5

<b>Course Code</b>	<b>3EC1224</b>
<b>Course Title</b>	<b>Minor Project</b>

**Course Learning Outcomes (CLOs):**

At the end of the course, students will be able to -

1. Identify the issues related to the recent trends in the field of embedded systems.
2. Formulate the problem definition, analyse and do functional simulation of the same.
3. Design, implement, test and verify the engineering solution related to the problem definition.
4. Compile, comprehend and present the work carried out.

A student is required to carry out project work in the relevant area of post-graduate study. The project work may include design / simulation / synthesis / development of a system / fabrication of prototype, etc. At the end of the semester, a student has to submit a detailed report incorporating literature survey, problem formulation, clear problem statement, research methods, result analysis, conclusion, etc. It is expected that the student should defend his/her work before the jury / panel of examiners.